#### Markov Network Based Equivalence Checking in Mixed-Signal Systems

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## Outline

#### Motivation

- Equivalence in AMS System
- Proposed Abstraction Markov Network
- Proposed Equivalence Checking
- Experimental Results
- Conclusion

## Motivation

- Check equivalence between two designs described on the same abstraction level and possibly on different levels
- What abstraction/representation can we use for Analog?



#### **State Space Representation**

- Dynamic system
  - □ State space equation
    - Represents a circuit as an ordinary differential equation (ODE) set
    - Describes a circuit's transient behavior
    - □ Vector fields dX/dt = f(X, u, t)



## **Markov Chain Representation**

- Dynamic system and Markov Chain
  - With discretization of both time and state value, it becomes Markov Chain
    - Also it could easily reflect PVT variations
  - $\square P(X_{next}|X_{current}) \text{ instead of } dX/dt = f(X,u,t)$



# Equivalence in Analog System

 When two systems' transition probabilities are same, two systems are equivalent

 $\Box \quad \text{Compare } P_{ref}(X_{next}|X_{current}) \text{ and } P_{dut}(X_{next}|X_{current})$ 

- However, as circuit size increases, the number of states to check grows exponentially !
  - A PLL with 300 nodes requires comparison of 300 dimensional transition probability



# **One Abstraction in Analog**

#### • Circuit graph

- Represents a circuit as a connectivity graph and check equivalence based on their graph structures
- Layout versus schematic (LVS)
- Observation
  - Not all circuit nodes are directly connected
  - Could we use this localization property to reduce complexity of comparing two high dimensional transition probabilities ?





#### Localization Property and Conditional Independence among Circuit Nodal Responses

Each nodal voltage/branch currents are unknowns or random variables



- A circuit is represented by a set of differential-algebraic equations that each equation is formed around these nodes
- With the given small time step, this set becomes algebraic equation set that could be separately solved when their boundary random variables are known – *conditional independence* !



#### **Markov Network Representation**

- Graphical model that includes all the conditional independences among circuit nodal responses
  - Each nodal voltage corresponds to a random variable
  - Construct an edge between two random variables when they directly interact
- Factorization property
  - Joint state distribution can be factorized into maximal cliques in the Markov network
  - Instead of comparing high dimensional transition probabilities, we can instead compare low dimensional factors of them !
  - $\square P(A_n, A_{n+1}, B_n, B_{n+1}, C_n, C_{n+1}, D_n, D_{n+1}, E_n, E_{n+1}, F_n, F_{n+1}) = K^* \Phi_1(A_n, A_{n+1}, B_n, B_{n+1}) \Phi_2(B_n, B_{n+1}, C_n, C_{n+1}) \Phi_3(C_n, C_{n+1}, D_n, D_{n+1}, E_n, E_{n+1}, F_n, F_{n+1})$



# **Learning Potentials**

Learning potentials is difficult task for a general graph
 There could be many different potentials for a given Markov network due to normalization [Sandberg]

 $p(\mathbf{X}) = \frac{1}{Z} \prod_{c \in \mathcal{C}} \psi_c(\mathbf{X}_c)$ 

- However, for a decomposable graph, potentials can be estimated as a marginal of clique member nodes
  - Normalization factor is not needed to be estimated

![](_page_9_Figure_5.jpeg)

P(A,B,C,D,E,F) = [P(A,B)P(B,C)P(C,D,E,F)] / [P(B)P(C)]

#### Markov Network Based Equivalence Checking

- Compare two systems' state distributions by comparing **factors** of two full joint distributions
  - Transition probabilities when inspecting dynamic behaviors
  - State distributions when inspecting static behaviors

![](_page_10_Figure_4.jpeg)

# **Network Reduction**

- Not every node in a circuit is independent random variable
  - In other words, it can be expressed as a function of other variables or just a constant
- Not every node in the Markov network is independent random variable
- Markov network can be reduced before transition probability comparison

![](_page_11_Figure_5.jpeg)

![](_page_11_Picture_6.jpeg)

![](_page_11_Picture_7.jpeg)

## **Entropy Based Network Reduction (1)**

Remove determinate nodes

- Determinate node settles to a certain value regardless of its initial state and it is determined by the other nodes' values -> can be removed
- Determinate node and Indeterminate node can distinguished by "X"
  - Settled / Unsettled  $\Leftrightarrow$  H(X)=0 / H(X)!=0  $\Leftrightarrow$  non-X / X

![](_page_12_Figure_5.jpeg)

### **Entropy Based Network Reduction (2)**

- Remove dependent nodes
  - □ If B=f(A), node B can be removed
  - Dependent Xs can be extracted by conditional entropy
    - $H(B|A)=0 \rightarrow B$  depends on A
    - $H(Y|X_1,...,X_k)=0 \rightarrow Y$  depends on  $X_1,...,X_k$

![](_page_13_Figure_6.jpeg)

#### Proposed Markov Network Based Equivalence Check

- 1. Extract a connectivity graph from two netlists, run short simulations with uniform random initial conditions and reduce the graphs by the entropy based method (Network reduction)
- 2. Graph isomorphism test (like LVS)
  - If they are different, report inequivalence
- 3. Triangularize the graph (make graph decomposable)
- 4. Learn clique marginals of the MN representations of circuits
- 5. Compare two corresponding clique marginals with Jensen–Shannon divergence

 $\Box \qquad JSD(P \mid\mid Q) = D(P \mid\mid M)/2 + D(Q \mid\mid M)/2, M = (P+Q)/2$ 

If any factor of two distributions are different more than the given threshold, report inequivalence

![](_page_14_Figure_9.jpeg)

# **Possible Applications**

- EX1) two circuit netlists with different process technologies such as 90nm and 130nm technologies
- EX2) schematic netlist and layout extracted netlist with parastics

![](_page_15_Figure_3.jpeg)

#### Coupled Ring Oscillator – Scaling from 0.13um to 90nm

![](_page_16_Figure_1.jpeg)

#### CMOS NAND Gate – Scaling from 0.13um to 90nm

![](_page_17_Figure_1.jpeg)

![](_page_17_Figure_2.jpeg)

# Coupled Ring Oscillator – Two Designs with Different W1:W2 Ratios

![](_page_18_Figure_1.jpeg)

#### Complexity of the Proposed Equivalence Checking Algorithm

Compared two designs with the same topology but with different sizes of transistors

	NAND	Oscillator	PLL
# of Circuit Nodes	4	6	374
# of Nodes after Reduction	4	6	225
# of Cliques in the MN	2	8	219
Maximum Clique Size in the MN	3	3	4
Average Clique Size in the MN	3	3	2.3
# of Cliques after Triangularization	2	2	192
Maximum Clique Size after Triangularization	3	5	5
Average Clique Size after Triangularization	3	5	2.9
# of Samples for 2 bit resolution for estimating potentials	64	1024	1024
Run Time of the Proposed Algorithm	13.1 (s)	148 (s)	1.2 (hour)

# Conclusion

- A way to establish the equivalence between two analog/mixed-signal circuits based on their Markov network representations is proposed
  - Check equivalence between two circuits with the same topology
  - □ issues
    - How to set threshold?
    - How to relate JSD distance to a circuit's performance metrics

![](_page_20_Figure_6.jpeg)

## **Additional Slides**

#### Follows

### **Possible Abstraction**

![](_page_22_Figure_1.jpeg)

# Learning the Potentials

#### The general recipe is:

- (1) For every maximal clique C, set the clique potential to its empirical marginal
- (2) For every intersection S between maximal cliques, associate an empirical marginal with that intersection and divide it into the potential of ONE of the cliques that form the intersection
- This will give ML estimates for decomposable Graphical Models
- In other words, for decomposable graph, we can acquire potential from empirical marginals for every maximal cliques and their interactions

#### Comparison of Two Decomposable Markov Networks

- MN1==MN2 ⇔ every maximal clique C1 in MN1 and C2 in MN2 of the same maximal clique C and its intersection S, P1(C)==P2(C) and P1(S)==P2(S)
  - MN1 == MN2 → P1(C)==P2(C) and P1(S)==P2(S) for every maximal cliques
  - P1(C)==P2(C) and P1(S)==P2(S) for every maximal cliques → construct potentials according to the recipe in the previous slide and derive canonical potentials from the previously constructed potentials
     → every of them is same → MN1==MN2

## **Decomposable Graph**

#### Chordal graph

A graph is chordal if each of its cycles of four or more nodes has a chord, which is an edge joining two nodes that are not adjacent in the cycle

![](_page_25_Figure_3.jpeg)

## Kullback–Leibler divergence

A non-symmetric measure of the difference between two probability distributions P and Q

$$D_{\mathrm{KL}}(P||Q) = -\sum_{x} p(x) \log q(x) + \sum_{x} p(x) \log p(x)$$
  
=  $H(P,Q) - H(P)$ 

http://en.wikipedia.org/wiki/KL\_divergence

## Jensen–Shannon divergence

A popular method of measuring the similarity between two probability distributions. It is also known as information radius (IRad). or total divergence to the average. It is based on the Kullback–Leibler divergence, with the notable (and useful) difference that it is always a finite value.

$$JSD(P \parallel Q) = \frac{1}{2}D(P \parallel M) + \frac{1}{2}D(Q \parallel M) \qquad M = \frac{1}{2}(P + Q)$$
$$0 \le JSD(P \parallel Q) \le 1$$

# Markov Network

- Undirected graphical model of a set of random variables
- Edges indicate dependency relationships between nodes (r.v.s)
  - □ Conditional independence (CI)
    - Ex) when B is observed, A and C are independent to each other
- Factorization property
  - The joint distribution of all variables can be decomposed to a set of potential functions among varaibles in maximal cliques (assuming strictly positive pdf)

 $(C,D,E,...) = \Phi_1(A,B)^* \Phi_2(B,C,D,E)^*...$ 

![](_page_28_Figure_9.jpeg)

## Factorization

Instead of a high dimensional joint distributions of all circuit node variables, it can be expressed as a set of decomposed low dimensional potentials of maximal cliques

![](_page_29_Figure_2.jpeg)

 $P(A,B,C,D,E) = \Phi_1(A,C,D) \Phi_2(B,C,D) \Phi_3(D,E)/Z$ 

#### **Factorization of Nodal Response Distribution**

- P1 = P(A2,A1,B2,B1,C2,C1,D2,D1) is directly estimated by samples
- P2 = P(A2,A1,B2,B1,C2,C1)\*P(A2,A1,D2,D1)/P(A2,A1)
- JSD(P1,P2) = 0.08671

![](_page_30_Figure_4.jpeg)

![](_page_30_Picture_5.jpeg)

#### **Factorization of Nodal Response Distribution**

![](_page_31_Figure_1.jpeg)

32

1.2

0.120

0.105

0.090

0.075

0.060

0.045

0.030

0.015

0.000

# Markov Network Representation – CMOS Inverter Example

- Each circuit nodes in the circuit netlist 
   Nodes in the Markov network
- Any connection via primitive instances such as resistor, capacitor or transistor 

   an edge in the Markov network
- Relationships between circuit nodes via nodal equations → probability relationships between nodes

![](_page_32_Figure_4.jpeg)

#### Markov Network Based Equivalence Checking

- Compare two systems' transition probabilities by comparing **factors** of two full joint distributions
  - If two full joints are same, any of its conditional distributions (i.e. transitional probabilities) are same as well
  - Static :  $P_{ref}(A,B,C)$  and  $P_{dut}(A,B,C)$
  - Dynamic :  $P_{ref}(A_1, A_2, B_1, B_2, C_1, C_2)$  and  $P_{dut}(A_1, A_2, B_1, B_2, C_1, C_2)$

![](_page_33_Figure_5.jpeg)

#### Ring Oscillator – ptm130 vs ptm090 / scale

![](_page_34_Figure_1.jpeg)

![](_page_34_Figure_2.jpeg)

# **Ring Oscillator – W1**

![](_page_35_Figure_1.jpeg)

![](_page_35_Figure_2.jpeg)

# of Circuit Nodes	6
# of Cliques in the MN	8
Maximum Clique Size in the MN	3
# of Cliques in the Junction Tree	2
Maximum Clique Size in the Junction Tree	5

(a)

![](_page_35_Figure_5.jpeg)

![](_page_35_Figure_6.jpeg)

# PLL – Divider Size

![](_page_36_Figure_1.jpeg)

2.5

(b)

3.0

3.5

MNs become non-isomorphic 0.35 06 0.30 05 0.25 04 0.20 USD 0.20 0.15 03 02 0.10 0.05 01 00∟ 0.0 0.00 0.5 1.0 1.5 2.0 2.5 3.0 3.5 0.5 1.0 1.5 2.0 L L

(a)

# of Circuit Nodes382# of Nodes after Reduction109# of Cliques in the MN119Maximum Clique Size in the MN4# of Cliques in the Junction Tree99Maximum Clique Size in the<br/>Junction Tree5